



Santana Row, 3031 Tisch Way, 110 Plaza West, PMB#: 220, San Jose, CA 95128

**POSITION:**

**Analog IC Design Engineer**

**RESPONSIBILITIES:**

- Perform Analog and Mixed-Mode IC design in CMOS
- Drive layout/CAD to GDSII release
- Work with analog, digital, and software teams to implement analog/mixed-signal circuits from initial definition and concept to full-production
- Document IP design, define test production/bench-level test/characterization plans, and conduct design reviews with team to demonstrate that the design meets specification

**REQUIRED QUALIFICATIONS:**

- BS or MS in EE (Mixed Signal Processing/Circuit Design) and 3+ years of experience in analog and mixed-mode circuit design
- Experience and knowledge of design and validation (e.g., worst-case, monte-carlo, temperature, and power supply simulations and CAD tools such as Cadence, Synopsys, Mentor Graphics, etc.) of ultra-low power voltage and current references (bandgap and CMOS), amplifiers, comparators, switch capacitor filters, DACs, ADC (SAR, FLASH, sigma-delta, etc.), and analog machine learning.
- Hands-on knowledge of analog concepts such as device sensitivities, offset and drift mismatch mitigation, linearity, AC and transient stability, low leakage, low-power and low-noise techniques
- IC design and development skills from definition to high-volume production release including design for manufacturability, test planning, layout supervision, debugging and lab testing (skills with lab equipment and data analysis), product engineering evaluation and characterization
- Good organization; written and verbal communication skills

**PREFERRED QUALIFICATIONS**

- Hands-on knowledge of analog IC design and verification tools
- Knowledge of MOSFET device level, transistor level circuit design, and analog IC design fundamentals.
- Contribute to design methodology, libraries, and code review
- Knowledge of machine learning (ML)