



Santana Row, 3031 Tisch Way, 110 Plaza West, PMB#: 220, San Jose, CA 95128

POSITION:

IC Product Test Engineer

RESPONSIBILITIES:

- Implement production test programs (DFT) and hardware (probe cards, load boards, sockets, ATC) for wafer probe and package test utilizing ATE equipment
- Work with Design to translate concepts to product testing requirements, develop documentation, load board and socket design specifications, and ATE final test programs
- Design IC test vectors for Power/Voltage/Temperature (PVT) corners, signal and power integrity characterization and participate in silicon debug, electrical failure analysis (EFA) and physical failure analysis (PFA)
- Work on wafer and package level test hardware and software development and bring up offshore test house by completing necessary correlation analysis
- Establish manufacturing requirements and specification for delivery of the product and drive to close potential test gaps and correlations issues across the product supply chain

REQUIRED QUALIFICATIONS:

- BSEE or higher
- 4+ years of experience in DFT, wafer probe card design, ATE hardware design, strong physical design and signal integrity experience on ATE load board/probe card design.
- Good organization; written and verbal communication skills

DESIRED QUALIFICATIONS:

- 4+ years of experience with scripting (C, C++, Python or Perl)
- 4+ years of experience with probability and statistical fundamentals for data analysis using tools such as JMP
- 4+ years of experience developing presentations for vendor and partner