Santana Row, 3031 Tisch Way, 110 Plaza West, PMB#: 220, San Jose, CA 95128

POSITION:

Vice President of Analog IC Design

RESPONSIBILITIES:

- hands-on VP research and development of analog mixed-mode Machine Learning (ML) integrated circuits targeting smart sensing IoT applications including but not limited to medical, industrial automation, robotics, and drone applications.
- Innovative role to solve technical problems, develop new sensing solutions that leverage in-depth analysis of analog signals among various front-ends (e.g., amplifiers, filters, AGC, mixers, switches, ADC, DAC, analog ML, etc.)
- Transform ASIC/SoC mixed-mode and digital solutions, including software, from ideas into products from inception to release to production
- Work closely with customers, suppliers (e.g., sensor, MEMS), product marketing, software/algorithm, digital IC design, CAD, and test and product engineering

REQUIRED QUALIFICATIONS:

- PhD or Master's Degree in EE/CE (Mixed Signal Processing/Circuit Design)
- Experience in several successful analog and mixed-mode ASIC products from concept to silicon release to production
- In-depth circuit-level understanding of analog front ends for various sensor interfaces, and the signal chain from sensors and transducers to digital systems such as embedded DSPs and MCUs
- Experience in architecting/designing ultra-low power mixed-signal sensing applications including design and verification, modeling for optimal analog-digital partitioning, cost/power optimizations, etc.
- Ability to operate within a lab to take bench measurements and debug hardware components at the IC and PCB level, e.g., analyzing analog and digital signals using lab equipment
- Experience in working with customers and product marketing from inception to analog and digital design, and CAD teams to release GDSII tape to fabrication through test, and product engineering teams to release chips to production
- Good organization; written and verbal communication skills

PREFERRED QUALIFICATIONS

- Knowledge of machine learning (ML)
- Experience in hands-on integrated circuits logic design, functional, and Power, Performance, Area (PPA) closure

- Machine Learning (ML) and related programming skills, e.g., in Python, MATLAB, C/C++
- Experience in code review, testing, and refactoring
- Good understanding of embedded CPU architecture such as ARM core/RISC-V core on: processing units, ISAs, pipelining, bus architectures as well as embedded memory components such as SRAM, and Non-Volatile Memory (EEPROM) from circuit and architecture perspective
- Skillful on data statistical analysis, aggregation, abstraction with knowledge of common ML frameworks
- Contribute to design methodology, libraries, and code review.